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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hiroshi HASHIMOTO et al.**

Group Art Unit: **2814**

Serial No.: **10/083,533**

Examiner: **Thao X. Le**

Filed: **February 27, 2002**

Confirmation No.: **6400**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING
A SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD
CHARACTERISTIC (As Amended)**

Attorney Docket Number: **020244**

Customer Number: **38834**

RESPONSE UNDER 37 C.F.R. § 1.111

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Date: May 2, 2005

Sir:

In response to the Office Action dated April 22, 2005, Applicants amend the claims as follows and submit the following remarks.

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 13 of this paper.